

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

MANUFACTURABLE RECESSED STRAINED RSD
STRUCTURE AND PROCESS FOR ADVANCED CMOS

Application Number :

10710738

Confirmation Number:

First Named Applicant:

Brian Messenger

Attorney Docket Number:

FIS920040004

Art Unit:

2814

Examiner:

MURSALIN B. HAFIZ

Search string:

(5155571 or 5218213 or 6358806 or 6420218 or 6504214 or 6621131 or
20010040292 or 20010048119 or 20030218212).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
mbh	1	5155571	1992-10-13	Wang et al.		257	19
mbh	2	5218213	1993-06-08	Gaul et al.		257	55
mbh	3	6358806	2002-03-19	Puchner		438	308
mbh	4	6420218	2002-07-16	Yu		438	142
mbh	5	6504214	2003-01-07	Yu et al.		257	347
mbh	6	6621131	2003-09-16	Murthy et al.		257	408

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
mbh	1	20010040292	2001-11-15	Hahn et al.		257	347
mbh	2	20010048119	2001-12-06	Mizuno et al.		257	192
mbh	3	20030218212	2003-11-27	Lee et al.		257	750

Signature

Examiner Name

Date

Mursalim Bin Hafiz

10/11/05